



09/940,901

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Certificate

JUN 14 2005

of Correction

Applicant(s): Roy E. Scheuerlein

Assignee: Matrix Semiconductor, Inc.

Title: INTEGRATED CIRCUIT INCORPORATING DUAL ORGANIZATION
MEMORY ARRAY

Patent No.: 6,889,307 B1

Issued: May 3, 2005

Atty. Docket No.: 023-0001

June 8, 2005

ATTN: Certificate of Correction Branch
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT –
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

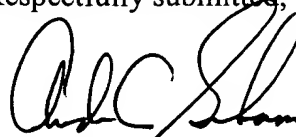
Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
Page 1, line 28; page 7, line 8 and line 16; page 8, line 15; page 10, line 7; page 11, line 27; and page 17, line 14.
3. Please send the Certificate to:

Andrew C. Graham
ZAGORIN O'BRIEN GRAHAM LLP
7600B N. CAPITAL OF TEXAS HWY, SUITE 350
AUSTIN, TEXAS 78731-1191

JUN 16 2005

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Andrew C. Graham', written in a cursive style.

Andrew C. Graham, Reg. No. 36,531

Attorney for Applicant(s)

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JUN 16 2005

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,889,307 B1

DATED : May 3, 2005

INVENTOR(S) : Scheuerlein

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Line 37: the words "the page An" should read -- the page. An --.

Column 4

Line 56: the word "maybe" should read -- may be --.

Line 67: the words "addresses A25-A2" should read -- addresses A25-A21 --.

Column 5

Line 43: the words " $2^{D-(W-X)}$ " should read -- $2^{(D-(W-X))}$ --.

Column 6

Line 51: the words "basic, page" should read -- basic page --.

Column 7

Line 54: the words "block 10" should read -- block 110 --.

Column 11

Line 38: the words "input./output" should read -- input/output --.

MAILING ADDRESS OF SENDER:
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PATENT NO. 6,889,307 B1

JUN 16 2005